

APPARATUS AND METHOD FOR CAPTURING THE
PROGRAM COUNTER ADDRESS ASSOCIATED WITH A
TRIGGER SIGNAL IN A TARGET PROCESSOR

Abstract of the Invention

1 In a target processor, trigger signals that result in
2 changes in the program execution must be identified and
3 communicated to a host processing unit. The target
4 processor uses the generation of a trigger signal by
5 preselected events to store the indicia of the preselected
6 events in a register for transfer to the host processing
7 unit. The program counter contents related to the trigger
8 signal is delayed to accommodate the target processor delay
9 and stored when a trigger signals is generated.